



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventors		
		MS1-151US
	Controlling Memory Usage in Sys	tems Having Limited Physical Memory
	TRANSMITTAL LE	ITER AND CERTIFICATE OF MAILING
То:	Commissioner of Patents and Washington, D.C. 20231	Trademarks,
From:	Daniel L. Hayes (Tel. 509-324-9256; Fax 509-928-2642) Lee & Hayes, PLLC W. 201 North River Drive, Suite 430 Spokane, WA 99201	
matter ide	entified in the above caption.  1. Transmittal letter including C  2. Specification—title page lis	ting inventors Sharad Mathur, Michael Ginsberg, Thomas H. Mitchell, and Jason Fuller, plus 29 pages, including claims 1-
]	Large Entity Status [x]	Small Entity Status [ ]
1	Date: <u>5/6/97</u>	Daniel L. Hayes Reg. No. 34,618
Service a an envelo below-inc	I hereby certify that the items list is either first class mail, or Expre ope addressed to The Commission	CERTIFICATE OF MAILING  sted above as enclosed are being deposited with the U.S. Postal as Mail if the blank for Express Mail No. is completed below, in the No. has also been marked on the listed items.  EM574494430
1	Date: May 6, 1997	By: Neles M. Have  Helen M. Hare